

## Senior Analog/Mixed-Signal Design Engineer

Location: Irvine, CA

### Description:

This role involves design and execution of circuits related to CMOS Image Sensor, such as ADC, DAC, Programmable Gain Amplifier, etc. The candidate will work closely with a team that has expertise in device physics, analog / digital circuit design, and image processing. This position requires close interaction with foundry, testing, packaging, and system house. Work with marketing / application engineers to translate product specifications in to circuit requirements and perform tradeoff analysis. Participate in system level architectural design of sensors and SOC.

### Requirements:

- BSEE required, MSEE preferred with minimum 5 years of experience.
- Proficient in CAD tools such as HSPICE, HSIM, Cadence.
- Experience in low-noise switch-capacitor circuit design necessary.
- Experience in layout is required.
- Proven track record of taking design to mass production is necessary, including chip debugging and yield improvement.
- Knowledge of ADC, PLL, DAC, Voltage Reference design is stressed.
- System-level knowledge is a plus.
- Experience in CMOS Image Sensor field / semiconductor device physics is a plus.
- Good communication skills, team player spirit.

Interested and qualified candidates please email your resume in MS Word format to:

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