Dr. George Cybenko
Dorothy and Walter Gramm
Professor of Engineering
Dartmouth College

George Cybenko is the Dorothy and Walter Gramm Professor of Engineering at Dartmouth. Professor Cybenko has made seminal research contributions in signal processing, neural computing, parallel processing and computational behavioral analysis. He was the Founding Editor-in-Chief of IEEE/AIP Computing in Science and Engineering and IEEE Security & Privacy. He has also served on the Defense Science Board (2008-2009) and on review and advisory panels for DARPA, IDA/CCS, NSA, BAE Systems and Lawrence Livermore National Laboratory. Professor Cybenko is a Fellow of the IEEE and received his BS (Toronto) and PhD (Princeton) degrees in Mathematics.

4PM, WEDNESDAY, JANUARY 12, 2011
Student Innovation Center (87-1600)
**NOTE CHANGE IN LOCATION THIS WEEK**